

ABSTRACT

An implementation of a technology, described herein, for efficient and flexible multi-bit halftoning on a marking device, such as a printer. At least one implementation of the technology, described herein, performs multi-bit halftoning with hardware (e.g., circuitry) rather than a programmable processor. Furthermore, at least one implementation performs multi-bit halftoning for a variable number of bits per pixel and a variable resolution. This abstract itself is not intended to limit the scope of this patent. The scope of the present invention is pointed out in the appending claims.

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